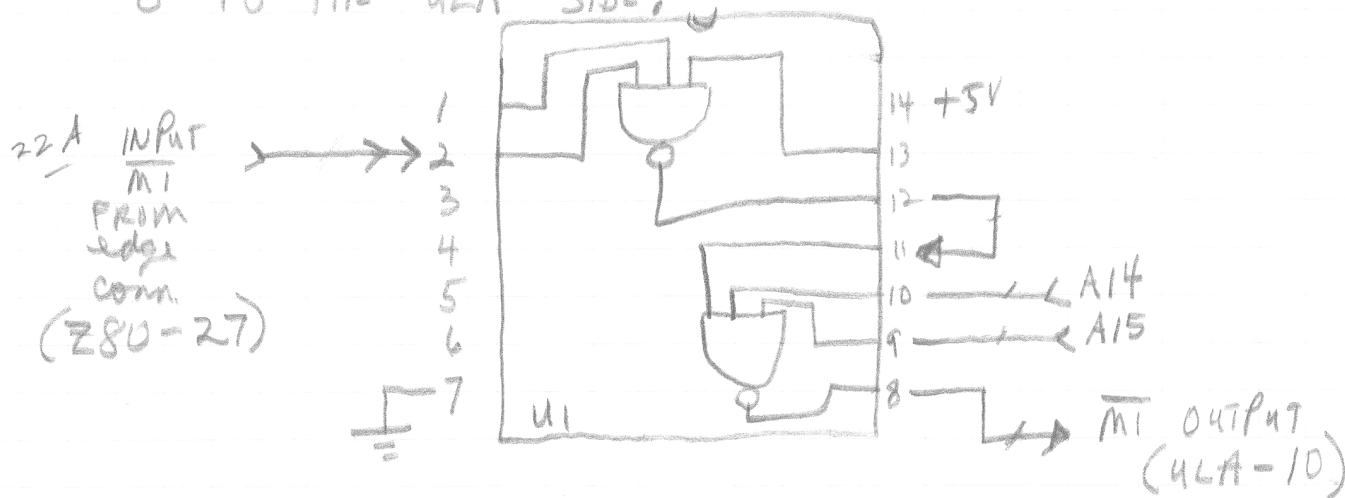


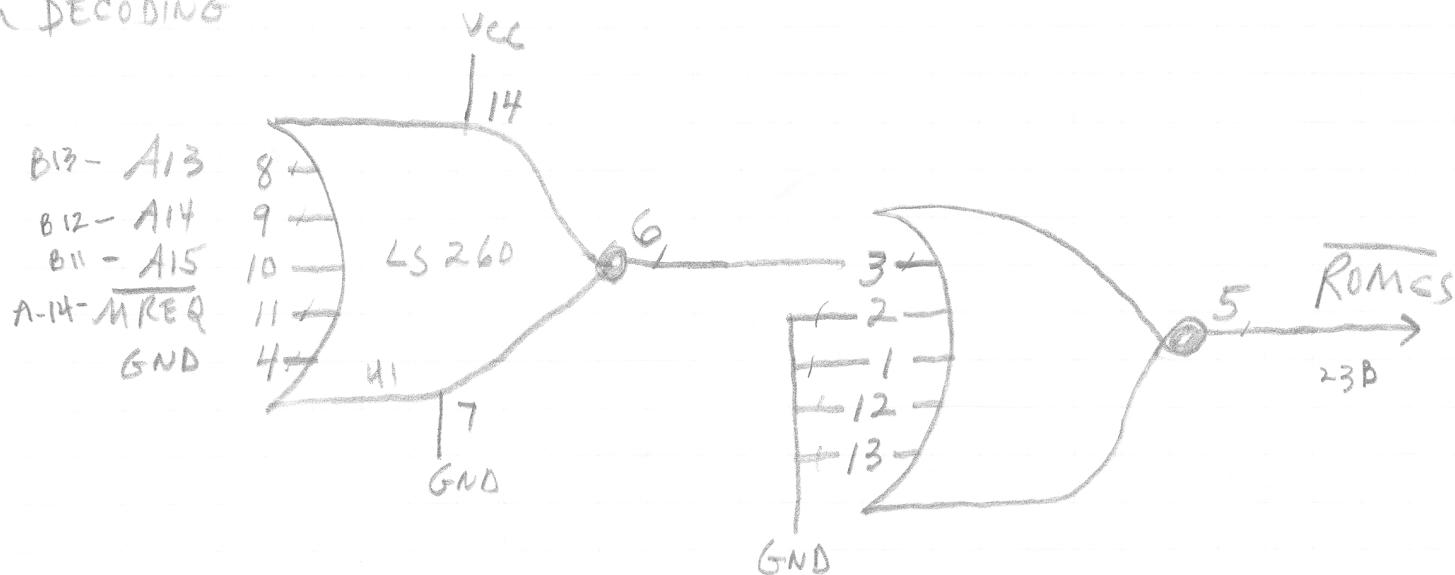
MC IN HIGH MEMORY

- (1) CUT \overline{M}_1 TRACE BETWEEN Z80-PIN 27 & ULA-PIN 10
- (2) CONNECT PIN 2 TO THE Z80 SIDE & THE OUTPUT ON PIN 8 TO THE ULA SIDE.

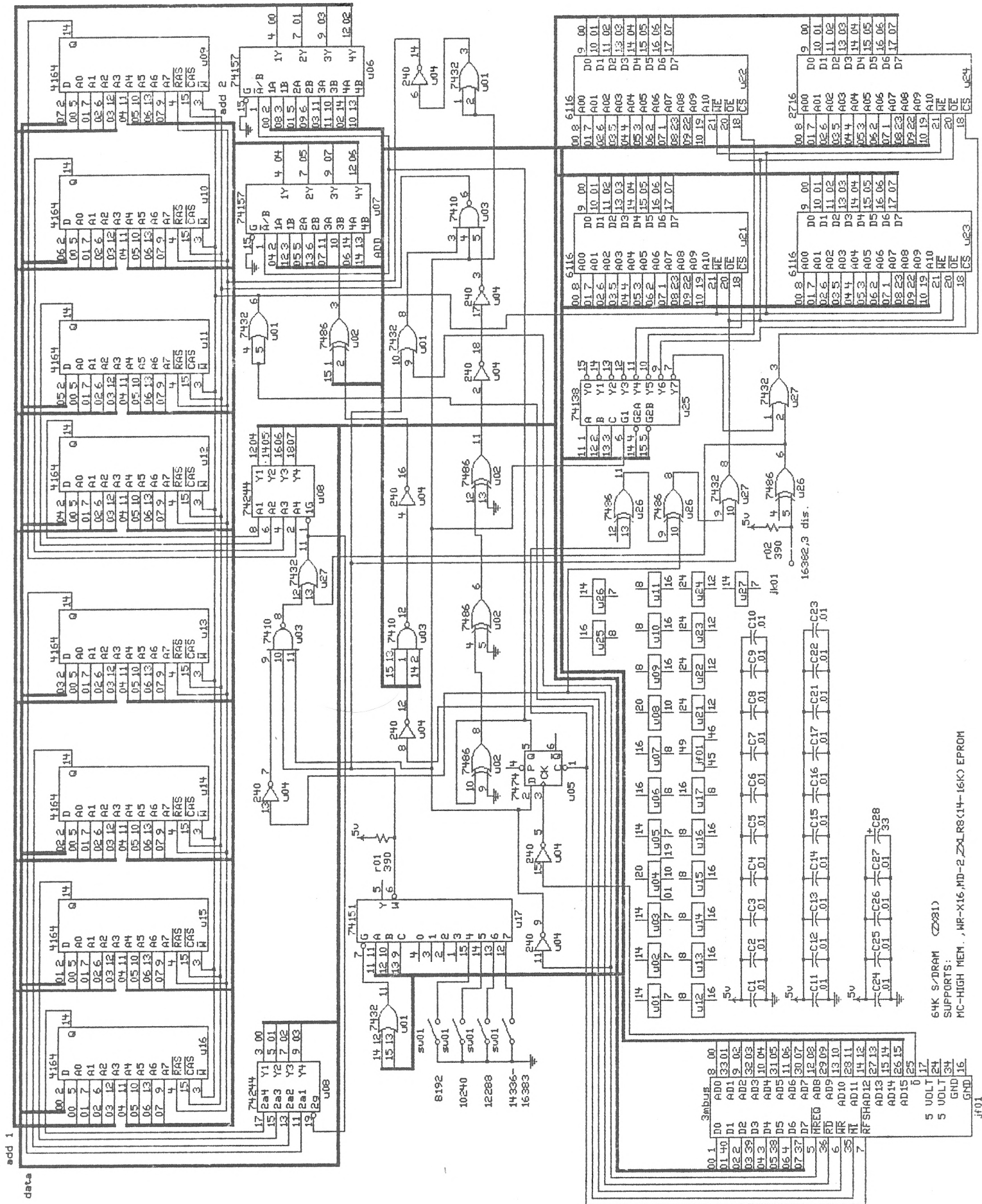


U1 74LS10

ROM DECODING



74LS260



64K S/DRAM (ZX81)

SUPPORTS:
MC-HIGH MEM., WB-X16, MD-2, ~~Z1~~ R8(14-16K) EPROM

j401