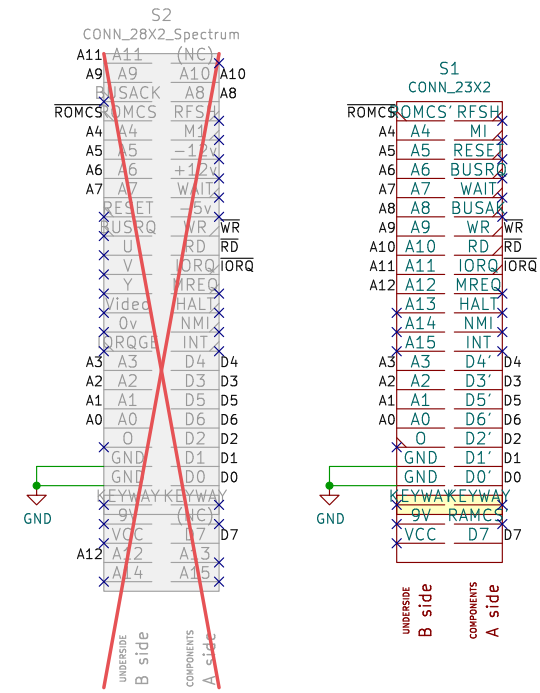


ZX SPECTRUM edge connector:

ZXB1 edge connector:



IORQ &1F (WR):Card Control Req.

D7	D6	D5	D4	D3	D2	D1	D0	
0	0	0	0	0	0	0	1	MEMORY BANK [RAM A13-14]
0	0	0	0	0	0	1	1	(NC)
0	0	0	0	0	0	0	0	RESET ADDRESS COUNTER
0	0	0	0	0	0	0	0	UM5100 PLAY
0	0	0	0	0	0	0	0	UM5100 RECORD
0	0	0	0	0	0	0	0	UM5100 RESET

IORQ &1F (RD):Card DATA VALUES.

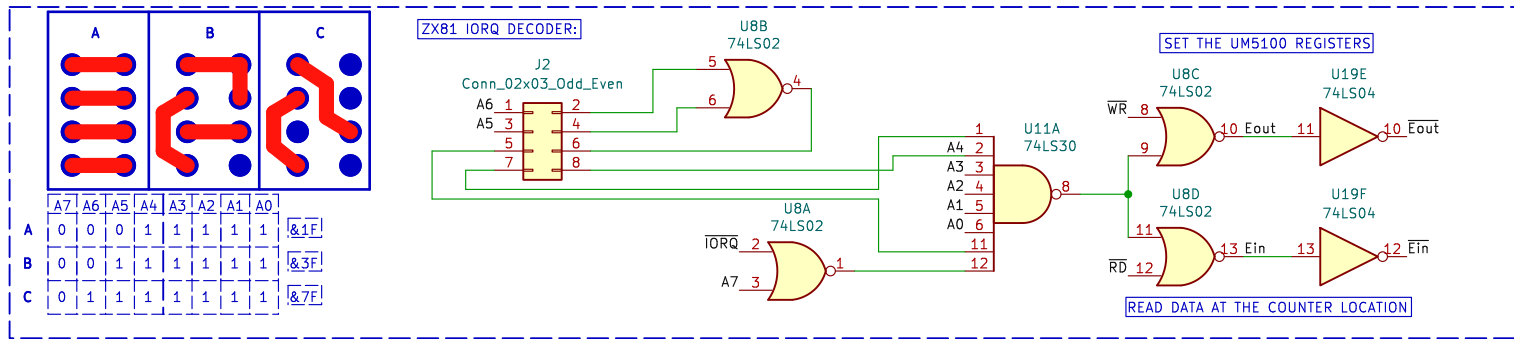
IN (\$1F) = LD A.(COUNTER)

Set card DATA VALUES LD (\$0000-\$2000).A

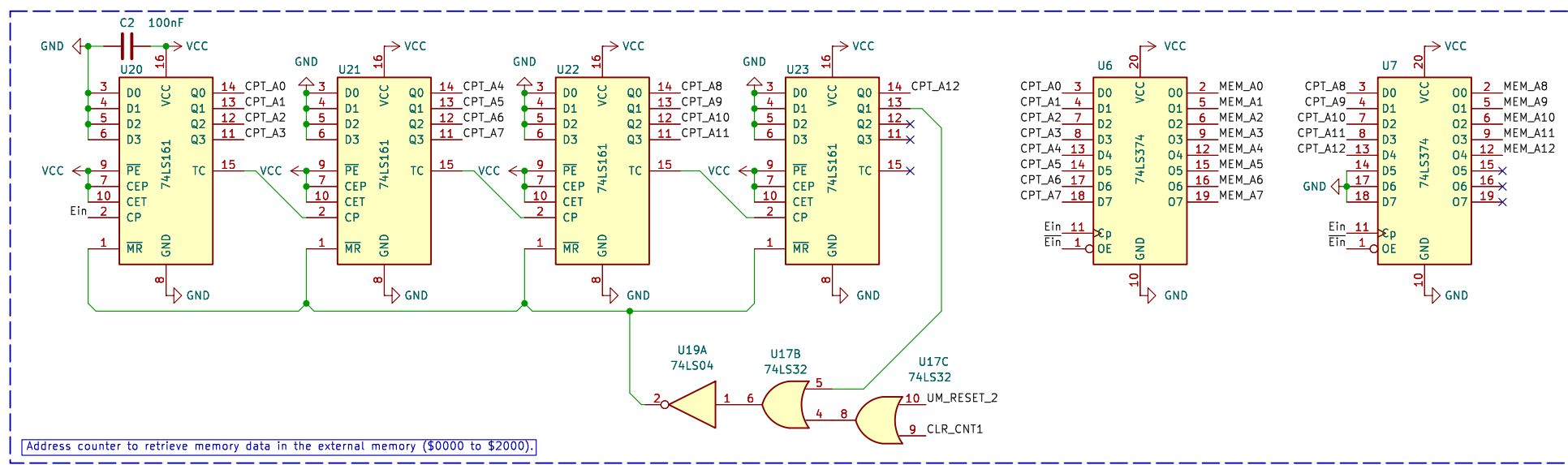
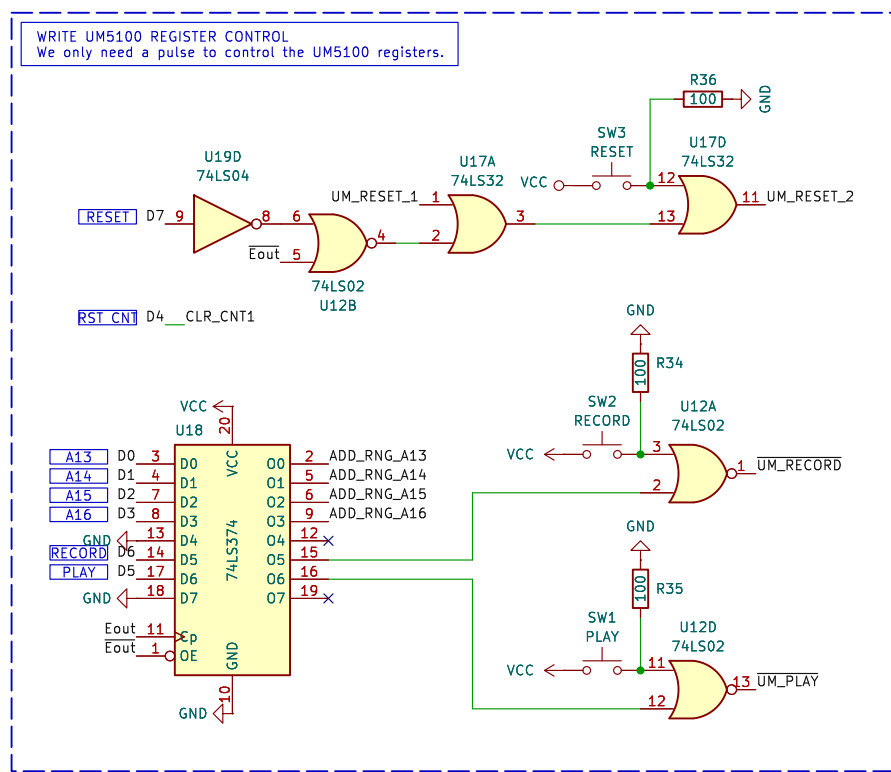
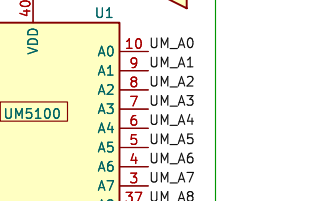
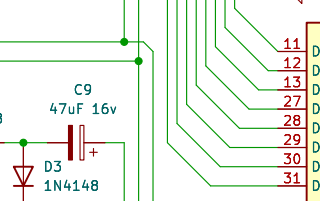
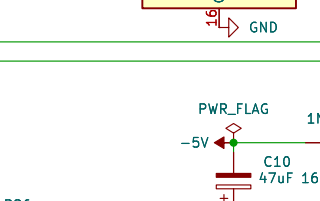
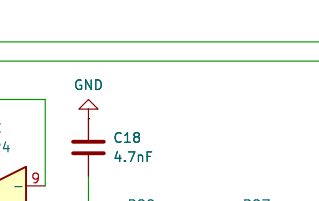
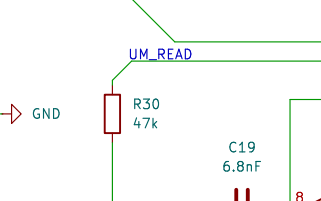
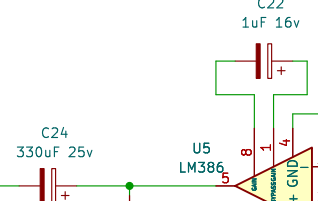
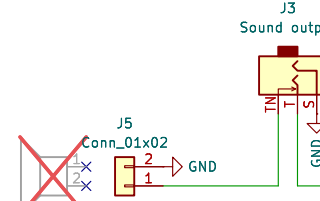
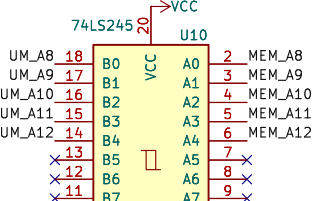
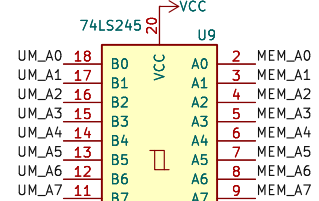
LOAD A BANK IN (\$1F) = LD A.(BANK+COUNTER).IN (J)+1

Memory Banks: (D0-3) = 16x8kBytes = 128kBytes

D3	D2	D1	D0		D3	D2	D1	D0	
0	0	0	0	\$0000-\$1FFF	8	1	0	0	\$10000-\$11FFF
1	0	0	0	\$2000-\$3FFF	9	1	0	0	\$12000-\$13FFF
2	0	0	1	\$4000-\$5FFF	10	1	0	1	\$14000-\$15FFF
3	0	0	1	\$6000-\$7FFF	11	1	0	1	\$16000-\$17FFF
4	0	1	0	\$8000-\$9FFF	12	1	1	0	\$18000-\$19FFF
5	0	1	0	\$A000-\$BFFF	13	1	1	0	\$1A000-\$1BFFF
6	0	1	1	\$C000-\$DFFF	14	1	1	1	\$1C000-\$1DFFF
7	0	1	1	\$E000-\$FFFF	15	1	1	1	\$1E000-\$1FFFF



UM5100 MEMORY BUS CONTROL



SPARE PARTS:

